

## Low Power DDR2 SDRAM

W97BH2KBV		
Datasheet	N/A	
Description	This is a 2Gb Low Power DDR2 SDRAM organized as 8M words x 8 banks x 32bits	
Features	Power supply VDD1 = 1.7V~1.95V、VDD2/VDDCA/VDDQ = 1.14V~1.30V Data width: x32 Burst Type: Sequential or Interleave、Clock rate : 400MHz, 533MHz Standard Self Refresh Mode PASR、ATCSR、Power Down Mode、DPD Programmable output buffer driver strength Four internal banks for concurrent operation Burst Length: 4、8 and 16 Operating Temperature Range: Extended (-25°C ~ 85°C), Industrial (-40°C ~ 85°C) Bidirectional, data strobe (DQS) is transmitted or received with data, to be used in capturing data at the receiver	
Diagram	N/A	
Package	134VFBGA(10mm x11.5mm), RoHS compliant	
Other Files	IBIS Model	N/A
	Spice Model	N/A
	Verilog Model	<a href="#">W97BHXK_verilog.zip</a>
Development Tools	N/A	
Others	KGD contact <a href="mailto:LPDRAM@winbond.com">LPDRAM@winbond.com</a>	

Contact us:No Contact